

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

HAYES ET AL.

Serial No.: 10/604,107

Filed: 26 JUNE 2003

**For: METHOD AND APPARATUS FOR
PERFORMING INPUT/OUTPUT FLOOR
PLANNING ON AN INTEGRATED
CIRCUIT DESIGN**

Attorney Docket No.: BUR920020055US1

Confirmation No.: 1106

Examiner: **PIERRE LOUIS, A.**

Art Unit: 2123

AMENDMENT UNDER 37 C.F.R. § 1.113

MS AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Please Enter
1/25/07
APL

Sir:

The present Amendment is submitted in reply to the Examiner's Final Office Action dated May 18, 2006, having a shortened statutory period set to expire August 18, 2006.